

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

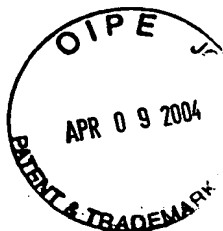
In re the Application of

Masahiro KANAI

Application No.: 10/733,455

Filed: December 12, 2003

For: NON-VOLATILE MEMORY DEVICE



Docket No.: 118072

INFORMATION DISCLOSURE STATEMENT

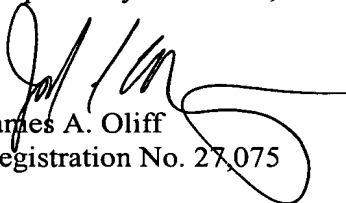
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. Relevance of the references Nos. 2 and 43-44 is discussed in the present specification.
- ☒ 3. The present application was filed or entered the U.S. National Stage of the PCT after June 30, 2003. In accordance with the June 11, 2003, Notice waiving the requirements of 37 CFR §1.98(a)(2)(i), copies of any U.S. patents and patent application publications are not attached.
- ☒ 4. English-language Abstracts of the non-English language references Nos. 43-47 are attached hereto.
- ☒ 5. A computer-generated English translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([<http://www.jpo.go.jp>]), and is attached, but has not been reviewed for accuracy. See References 43-47.

Respectfully submitted,



James A. Oliff
Registration No. 27,075

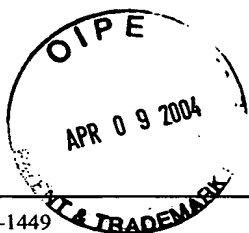
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JAO:JSA/tmw

Date: April 9, 2004

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<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
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PATENT & TRADEMARK OFFICEATTY DOCKET NO.
118072APPLICATION NO.
10/733,455

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT
Masahiro KANAIFILING DATE
December 12, 2003

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1	6,255,166 B1	07/03/2001	Ogura et al.		
	2	5,408,115	04/18/1995	Chang		
	3	5,696,383	10/19/1999	Chang et al.		
	4	5,422,504	06/06/1995	Chang et al.		
	5	5,494,838	02/27/1996	Chang et al.		
	6	6,177,318 B1	01/23/2001	Ogura et al.		
	7	6,248,633 B1	06/19/2001	Ogura et al.		
	8	6,587,381	07/01/2003	Kanai et al.		
	9	6,587,380	07/01/2003	Kanai et al.		
	10	6,646,916	11/11/2003	Kamei		
	11	2003-0072191	04/17/2003	Kamei		
	12	2003-0072194	04/17/2003	Kamei		
	13	6,707,720	03/16/2004	Kamei et al.		
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	15	2002-0191453	12/19/2002	Owa		
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	17	2003-0025150	02/06/2003	Kanai et al.		
	18	2003-0020123	01/30/2003	Kanai		
	19	2003-0048674	03/13/2003	Kanai		
	20	2003-0034530	02/20/2003	Kamei		
	21	2003-0031064	02/13/2003	Natori		
	22	2003-0027411	02/06/2003	Kanei		
	23	2003-0025149	02/06/2003	Kanei		
	24	2003-0123303	07/03/2003	Kamei		
	25	2003-0179609	09/25/2003	Natori		
	26	2003-0095443	05/22/2003	Natori		
	27	2003-0076717	04/24/2003	Natori		
	28	2003-0103388	06/05/2003	Natori		

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	29	2003-0137875	07/24/2003	Natori		
	30	2003-0146450	08/07/2003	Natori		
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	40	2004-0013027	01/22/2004	Kanai		
	41	2004-0061139	04/01/2004	Natori		
	42	10/728,746	12/08/2003	Natori		

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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
	43	JP A 6-181319 (with abstract and translation)	06/28/1994	Japan		
	44	JP A 11-74389 (with abstract and translation)	03/16/1999	Japan		
	45	JP B1 2978477 (with abstract and translation)	09/10/1999	Japan		
	46	JP A 7-161851 (with abstract and translation)	06/23/1995	Japan		
	47	JP A 2001-156188 (with abstract and translation)	06/08/2001	Japan		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	48	Hayashi et al. "Twin MONOS Cell with Dual Control Gates", 2000 Symposium on VLSI Technology Digest of Technical Papers, 2000 IEEE
	49	Chang et al. "A New SONOS Memory Using Source-Side Injection for Programming", IEEE Electron Device Letters, Vol. 19, No. 7, July 1998, pp 253-255
	50	Chen et al. "A Novel Flash Memory Device with Split Gate Source Side Injection and ONO Charge Storage Stack (SPIN), 1997 Symposium on VLSI Technology Digest of Technical Papers", pp 63-64

EXAMINER

DATE CONSIDERED

Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Date: April 9, 2004